# Build in Biasing Circuit MOS FET IC UHF RF Amplifier

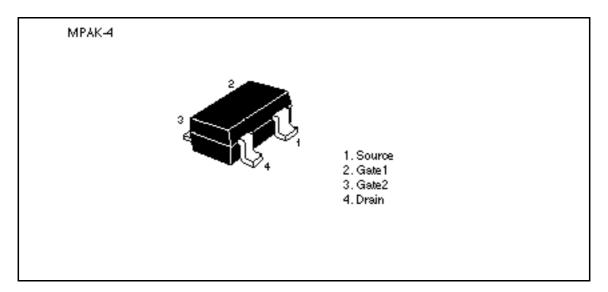


ADE-208-504 1st. Edition

#### Features

- Build in Biasing Circuit; To reduce using parts cost & PC board space.
- Low noise characteristics; (NF = 2.0 dB typ. at f = 900 MHz)
- Withstanding to ESD; Build in ESD absorbing diode. Withstand up to 200 V at C = 200 pF, Rs = 0 conditions.

#### Outline





## **Absolute Maximum Ratings** (Ta = $25^{\circ}$ C)

Item	Symbol	Ratings	Unit		
Drain to source voltage	V <sub>DS</sub>	6	V		
Gate 1 to source voltage	$V_{g1S}$	+6 -0	V		
Gate 2 to source voltage	V <sub>G2S</sub>	±6	V		
Drain current	Ι <sub>D</sub>	25	mA		
Channel power dissipation	Pch	150	mW		
Channel temperature	Tch	150	°C		
Storage temperature	Tstg	-55 to +150	°C		

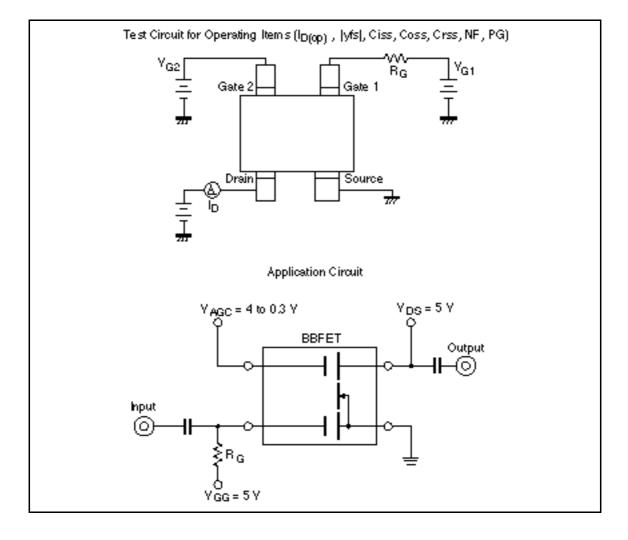
## **Electrical Characteristics** (Ta = 25°C)

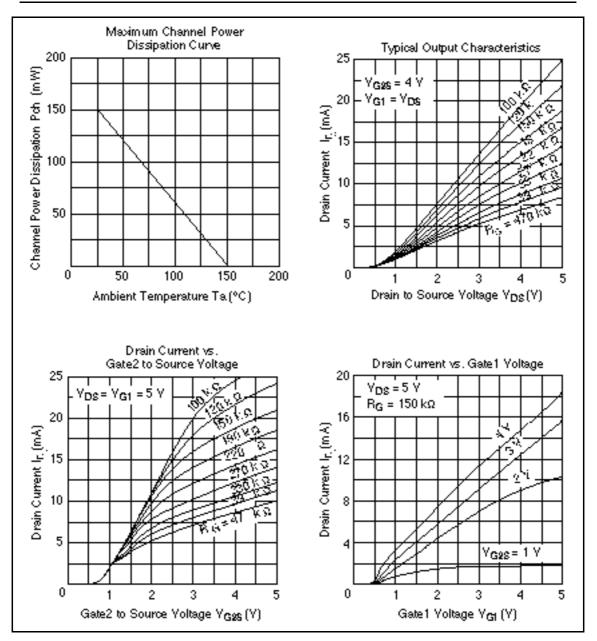
Item	Symbol	Min	Тур	Max	Unit	Test conditions
Drain to source breakdown voltage	$V_{\rm (BR)DSS}$	6		_	V	$I_{D} = 200 \ \mu A$ $V_{G1S} = V_{G2S} = 0$
Gate 1 to source breakdown voltage	$V_{\rm (BR)G1SS}$	+6	—	_	V	$I_{G1} = +10 \ \mu A$ $V_{G2S} = V_{DS} = 0$
Gate 2 to source breakdown voltage	$V_{(BR)G2SS}$	±6	—	_	V	$\begin{split} I_{\text{G2}} &= \pm 10 \; \mu\text{A} \\ V_{\text{G1S}} &= V_{\text{DS}} = 0 \end{split}$
Gate 1 to source cutoff current	I <sub>G1SS</sub>	_	—	+100	nA	$V_{G1S} = +5 V$ $V_{G2S} = V_{DS} = 0$
Gate 2 to source cutoff current	I <sub>G2SS</sub>		_	±100	nA	$V_{G2S} = \pm 5 V$ $V_{G1S} = V_{DS} = 0$
Gate 1 to source cutoff voltage	$V_{\text{G1S(off)}}$	0.2	_	0.8	V	$V_{_{DS}} = 5 \text{ V}, \text{ V}_{_{G2S}} = 4 \text{ V}$ $I_{_{D}} = 100 \mu\text{A}$
Gate 2 to source cutoff voltage	$V_{\text{G2S(off)}}$	0.4	_	1.0	V	$V_{_{DS}} = 5 \text{ V}, V_{_{G1S}} = 5 \text{ V}$ $I_{_{D}} = 100 \mu\text{A}$
Drain current	I <sub>D(op)</sub>	10	15	20	mA	$V_{DS} = 5 V, V_{G1} = 5 V$ $V_{G2S} = 4 V, R_{G} = 220 k$
Forward transfer admittance	y <sub>fs</sub>	16	22	—	mS	$V_{DS} = 5 V, V_{G1} = 5 V, V_{G2S} = 4 V, R_G = 220 k$ , f = 1 kHz
Input capacitance	Ciss	1.2	1.7	2.2	pF	$V_{\rm DS} = 5  V,  V_{\rm G1} = 5  V$
Output capacitance	Coss	0.7	1.1	1.5	pF	V <sub>G2S</sub> = 4 V, R <sub>G</sub> = 220 k
Reverse transfer capacitance	Crss	_	0.012	0.03	pF	f = 1 MHz
Power gain	PG	16	20	—	dB	V <sub>DS</sub> = 5 V, V <sub>G1</sub> = 5 V V <sub>G2S</sub> = 4 V
Noise figure	NF	_	2.0	3.0	dB	$R_{g} = 220 \text{ k}$ , f = 900 MHz

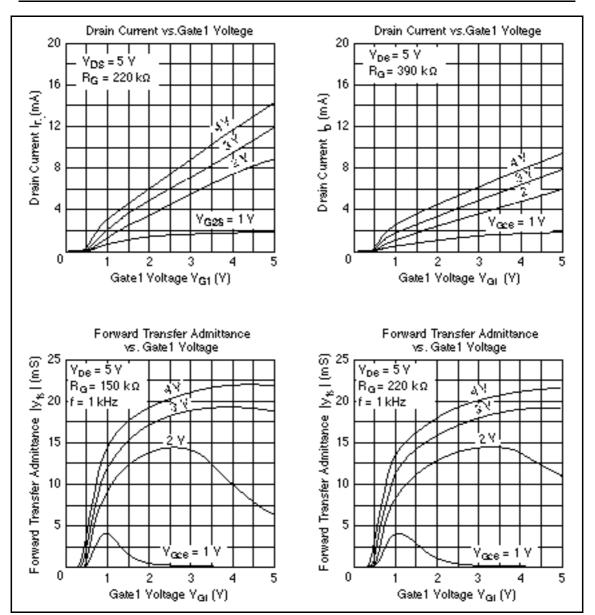
Note: Marking is "AU-".

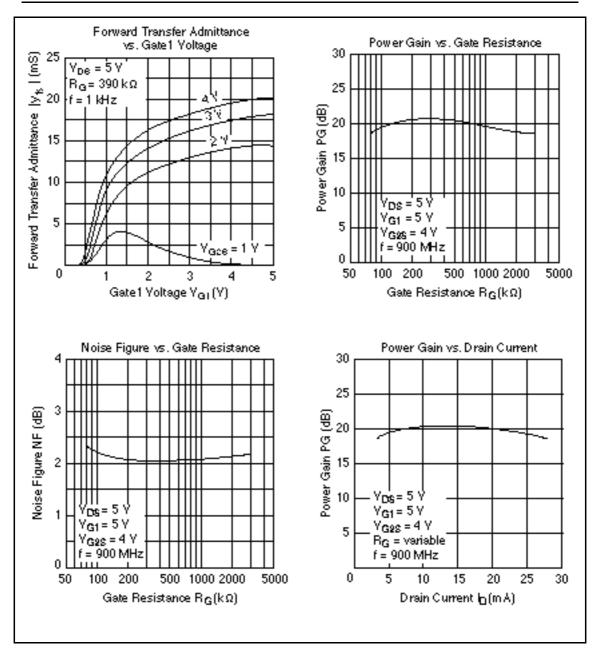
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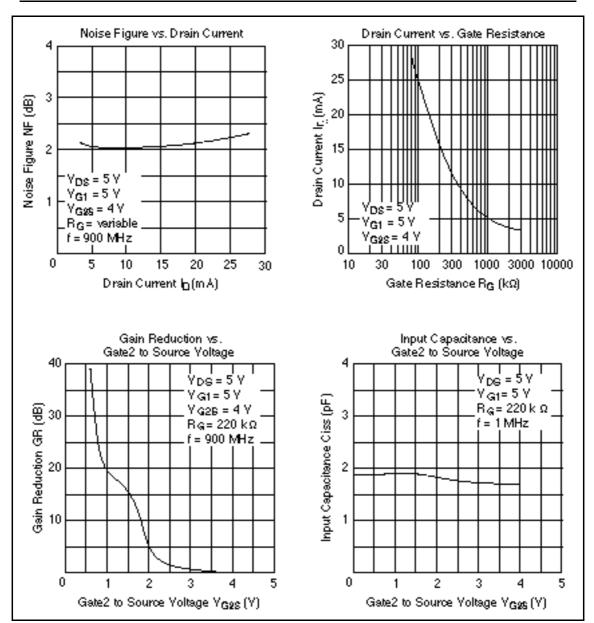
#### **Main Characteristics**

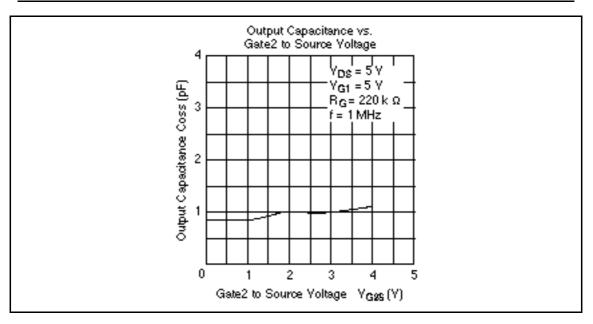






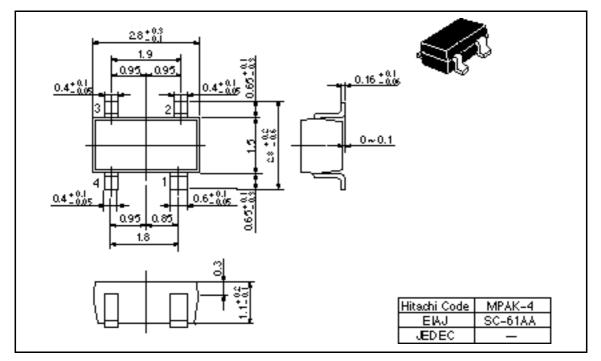






# **Package Dimentions**

Unit: mm



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